

FIG. 1 is a block diagram of a spread spectrum communication system. The system includes a message data input, a data modulator (51), a spread-spectrum transmitter (52), a communications channel (53), a spread-spectrum demodulator (54), a square-law device (55), a bandpass filter (56), a frequency divider (57), a synchronous data demodulator (60), and a received data output.

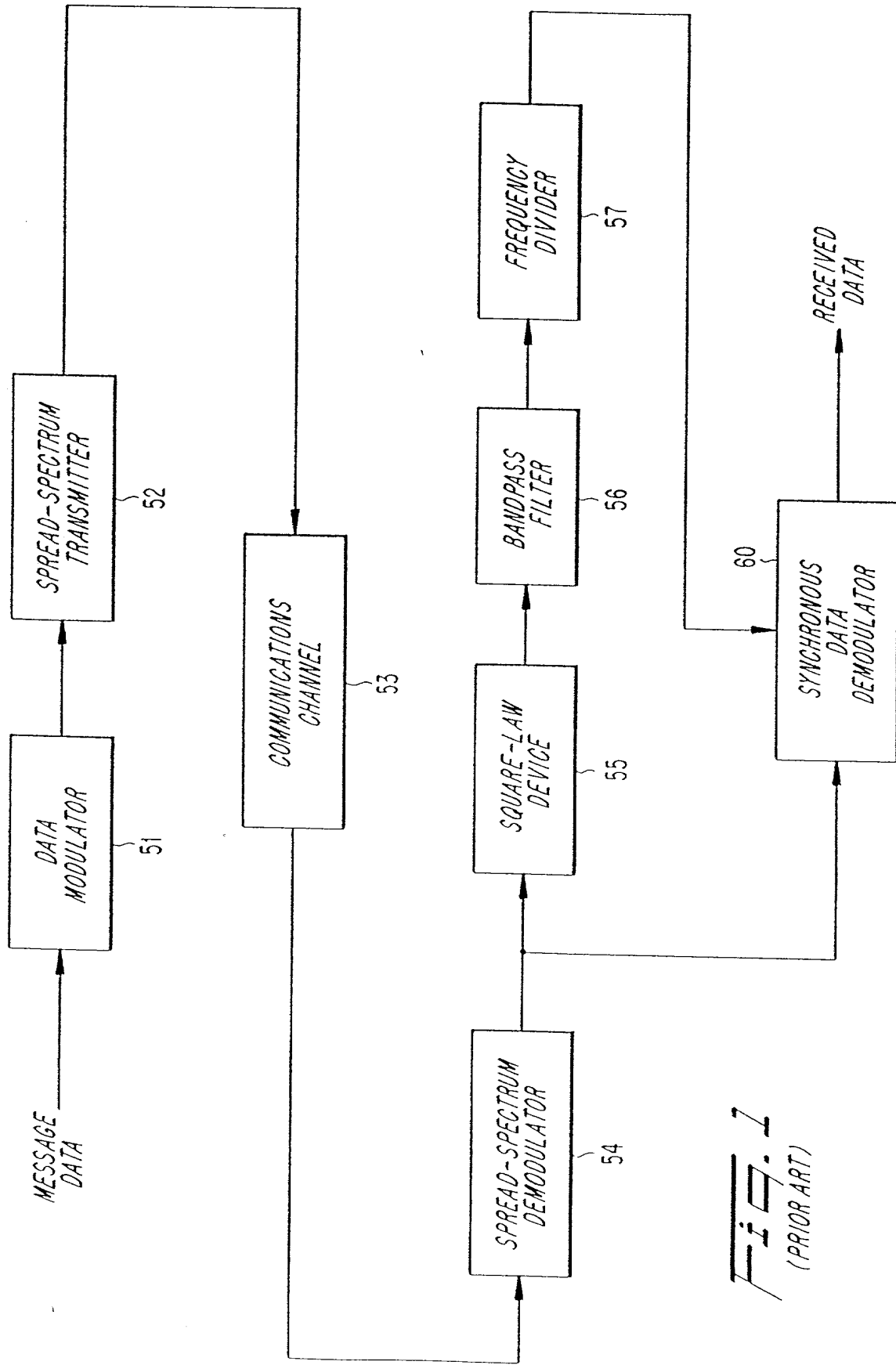


Fig. 1
(PRIOR ART)

Fig. 2

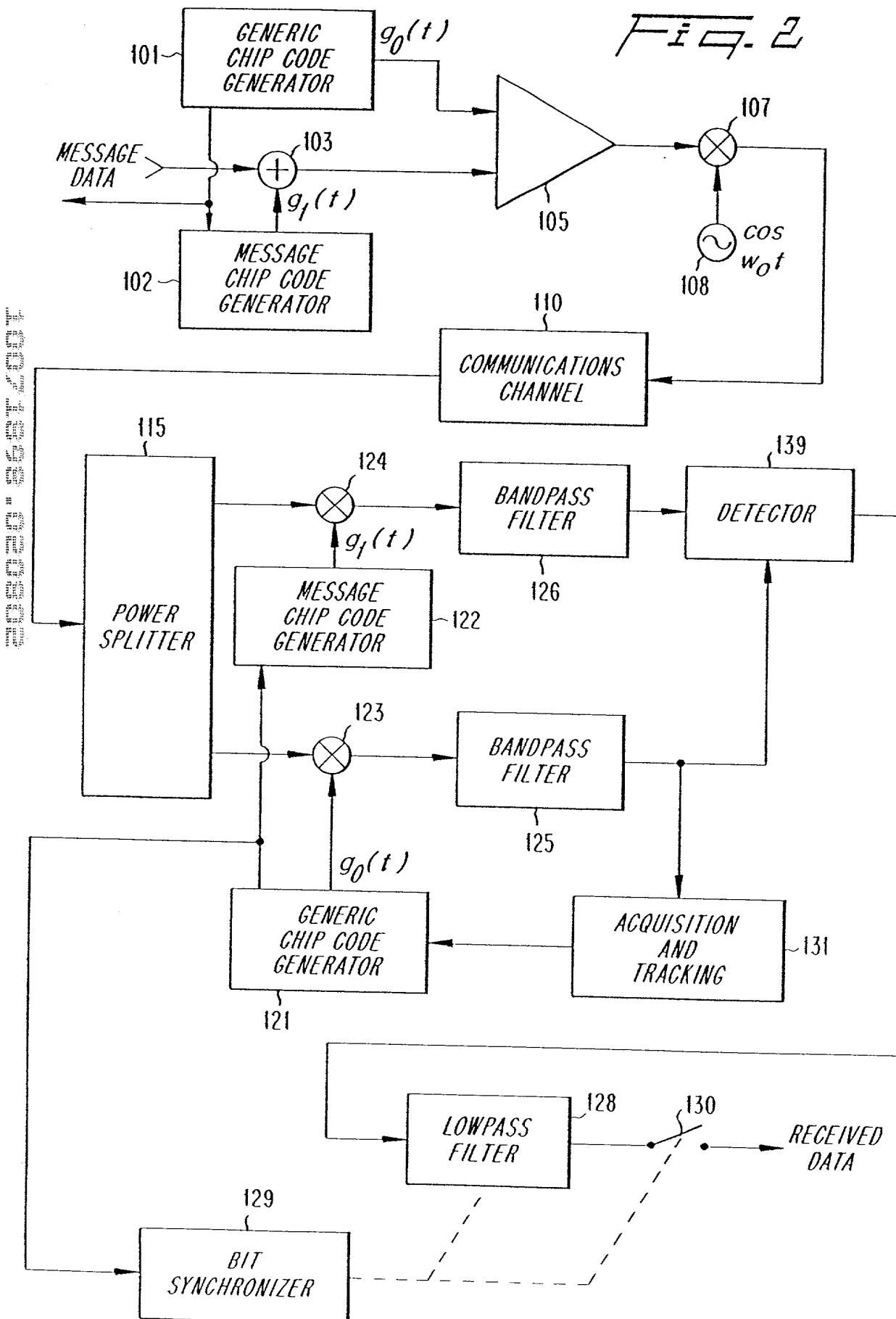
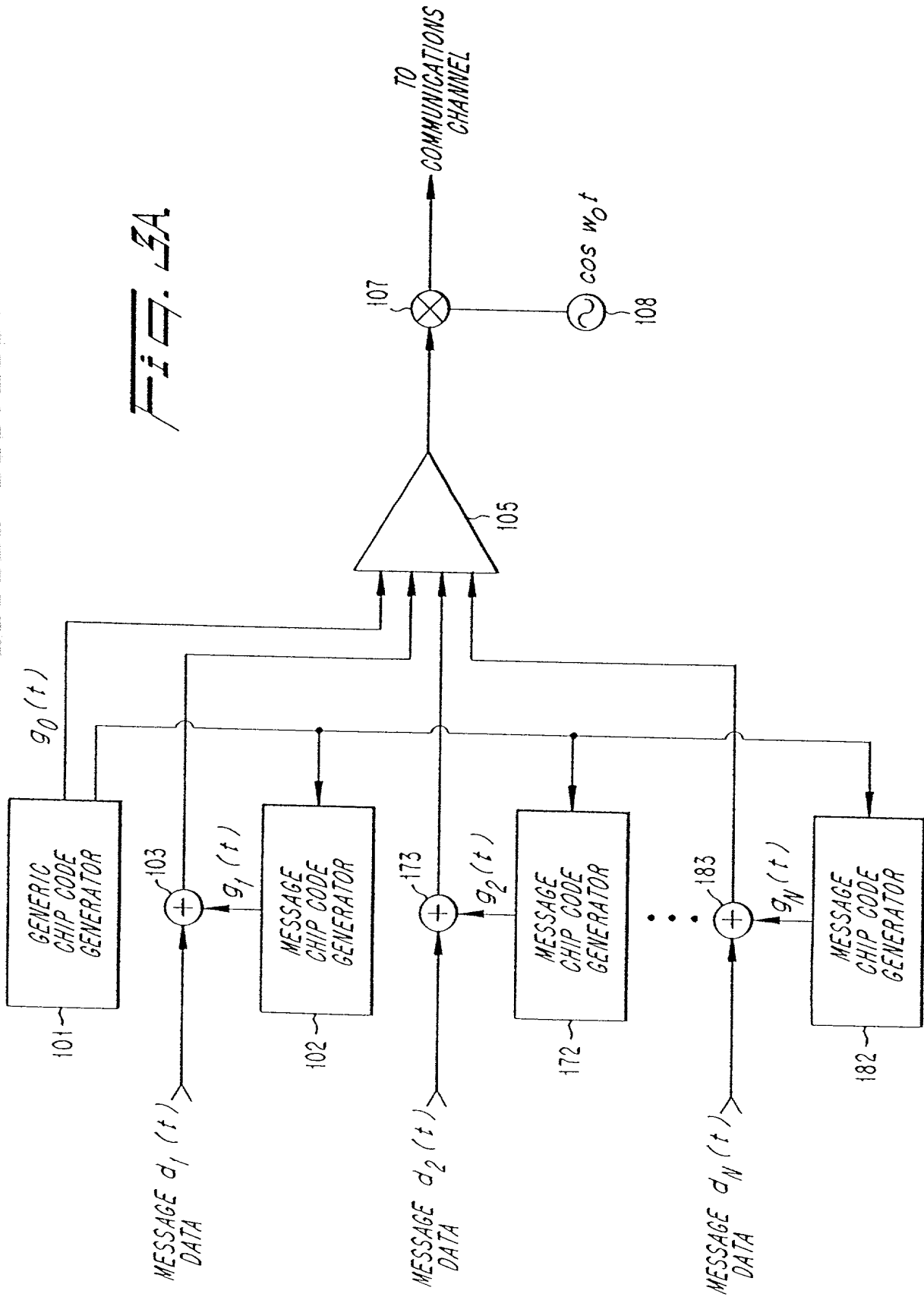
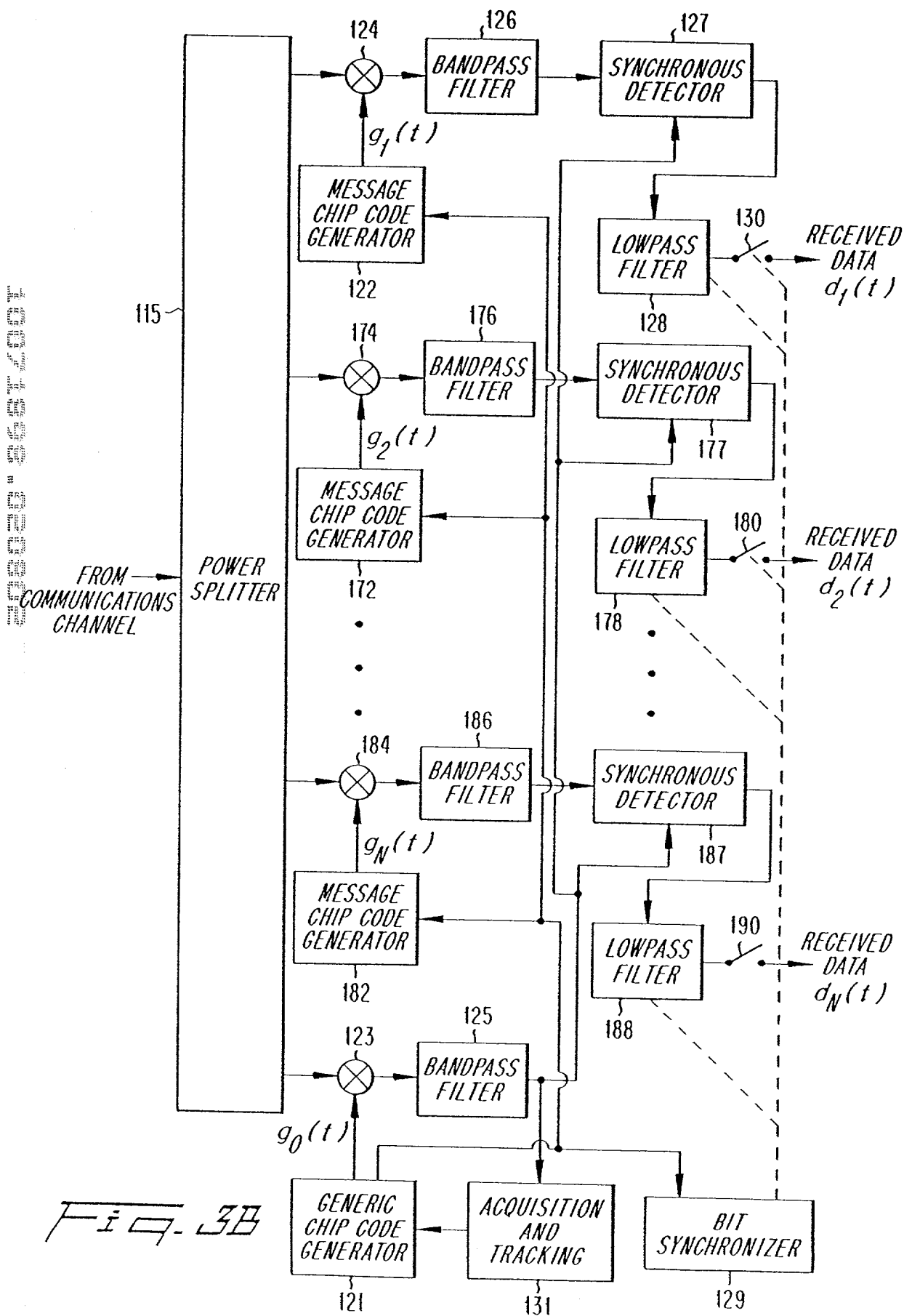


FIG. 3A





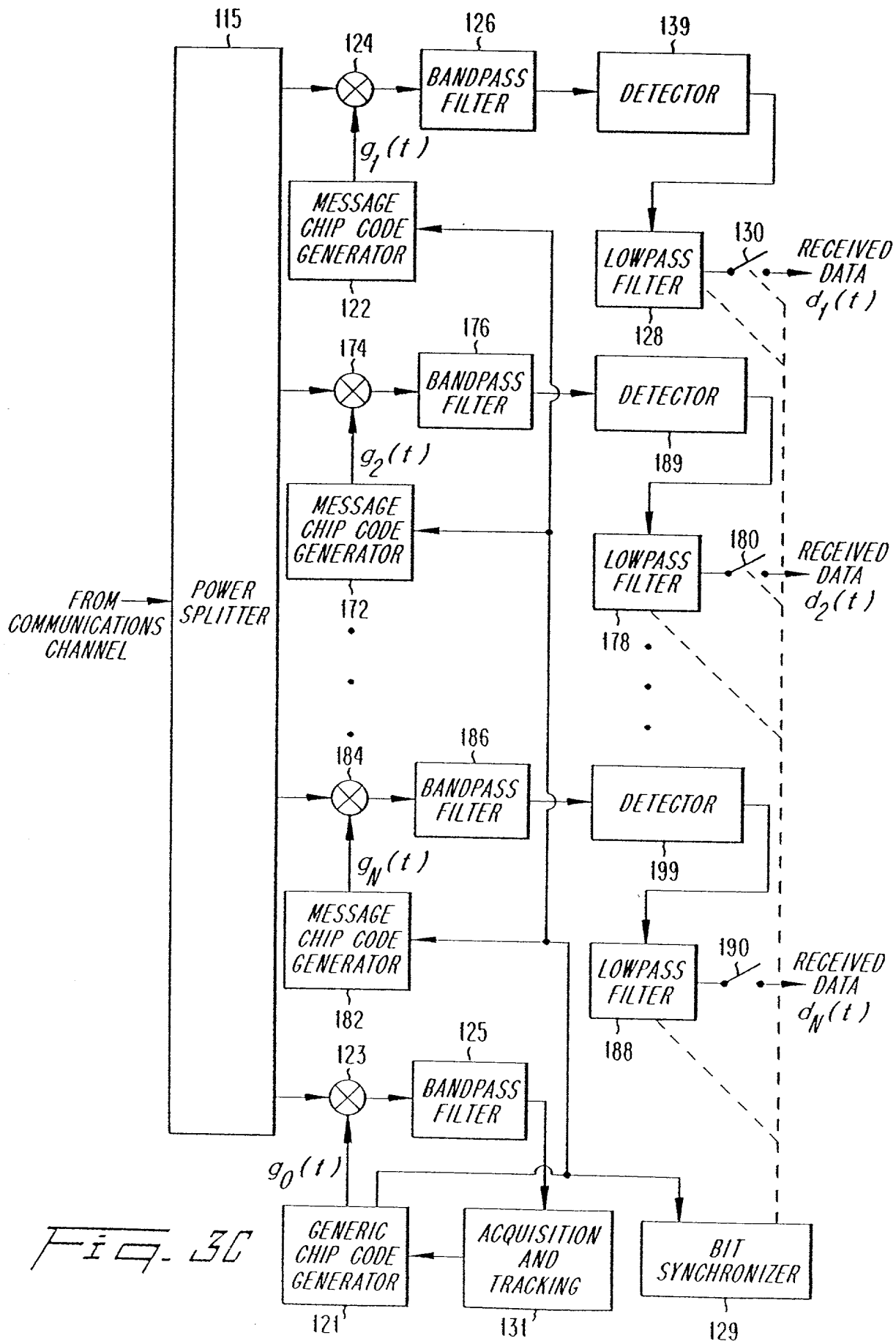


Fig. 4

